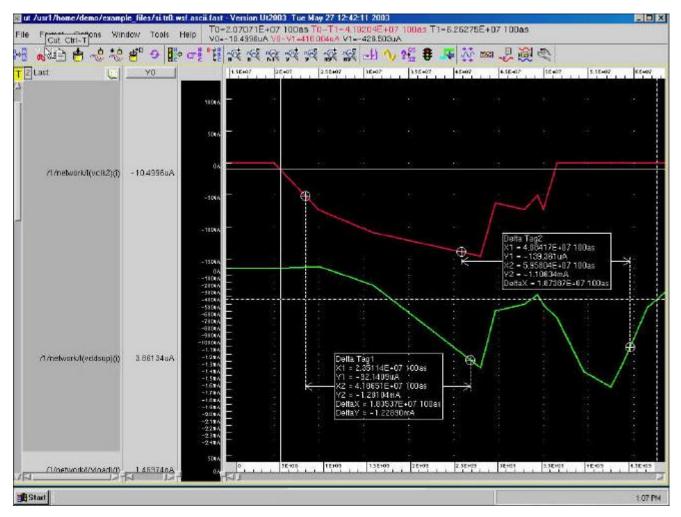# vWave

the EDA market's fastest and most powerful universal analog and digital waveform viewer

One tool provides a complete set of features for both analog & digital:

- **Very High Speed File Loading**

- **Very High Speed Signal Display**

- **Complete Set of Analog Functions**
  - **Arithmetic Functions**
  - **Trigonometry Functions, FFT, DFT**
  - **Integrate/Differentiate Functions**
  - **Filter, Elliptic, Bessel, Butterworth**
  - **Chebyshev, FIR, IIR, etc.**
  - **Frequency, Jitter, Period verses Time**

- **Perl, TCL, Measures HSpice* Scripting**

- **File Compression up to over 1000x**

- **Can be run in mixed mode with Verilog, VHDL, and SystemVerilog, SV Assertions**

- **Supports: HSpice, NanoSim, FineSim, UltraSim, Spectre, HSIM, and others**

- **vWave will significantly reduce your EDA costs**

Veritools

Tools for Power Users Doing Analog/Digital Designs

vWave is a powerful waveform viewing and analysis tool, and provides direct viewing of both analog, digital and mixed analog/digital files.

vWave – provides the fastest wave form display available today on the market, from even very large files. Waveforms can be displayed from even very large files in just seconds.

vWave can simultaneous load and display an unlimited number of waveform files both analog and digital at the same time.

vWave supports both analog and digital waveforms with the most complete set of analysis tools available including analog analysis, HSpice Measure calculator, Logic analyzer, built in Perl and TCL scripting. **Arithmetic Functions** , include Trigonometry Functions, FFT, DFT , Integrate/Differentiate Functions, Filter, Elliptic, Bessel, Butterworth, Chebyshev, FIR, IIR, and Frequency, Jitter, Period verses Time

vWave directly supports the following formats directly:

PLI, VPI, VHPI, VCD, utF (Veritools Compressed format), TR0, PSF ASCII, PSF Binary, AC Sweep, DC Sweep, FT0, .out, CSDF-TR0, CDSF-AC Sweep, CSDF-DC Sweep, .DOU, .COU.

vWave supports the following simulators; **Cadence NCSim, Synopsys VCS, Modeltech ModelSim, HSpice,** **NanoSim, HSIM, FineSim, UltraSim and others**

vWave compressed formats, API, PLI, VPI, VHPI, support file compressions of up to 4000 times.

vWave supports powerful Scripting capability based on PERL/TK and TCL/TK and HSpice* Measures: The vWave scripting language is based on the CAD industry standard scripting languages, PERL and TCL/ TK and HSpice* Measures. Users can write scripts which can be used for either digital or analog analysis. Scripts can be run interactively or in batch mode using any of the Veritools databases.

vWave is integrated into **VeritoolsDesigner** and **VeritoolsVerifyer**

VeritoolsDesigner provides RTL Source Code debugging with Trace Input Cone, Control Flow Graph: vWave allows users to trace signal values in their designs schematically, even through multiple levels of FFs. Users can also display a complete control flow graph of their RTL design, with all FFs shown and the time selected signal values passed through these FF's. These signal values can automatically traced back to the logic in the design where the error condition occurred.

VeritoolsVerifyer provides a **SV Assertions simulator** so users can verify the functionality of their design quickly.

Veritools provides the world's most powerful tools for analog, digital and mixed-mode designers while reducing your EDA costs