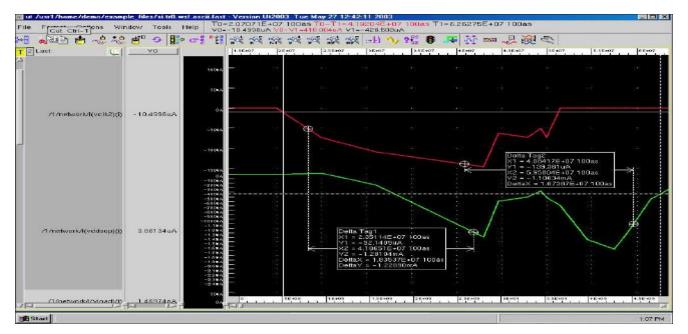
vWave – Universal Complete features for both analog and digital waveform viewing on Linux, now runs native on Windows



The only universal tool that provides a complete set of features for both analog & digital waveform viewing:

• EDA's only universal waveform display, for Native Windows and Linux, Centos 5, 6, 7, Ubuntu

• Provides a complete set of both Analog and Digital functions and features

- Arithmetic Functions
- Trigonometry Functions, FFT, DFT

Integrate/Differentiate
Functions
Filter, Elliptic, Bessel,
Butterworth
Chebyshev, FIR,
IIR, etc.

• Frequency, Jitter, Period verses Time

- Perl and Python Scripting
- File Compression up to over 1000x
- Can be run in mixed mode with Verilog, VHDL, and SystemVerilog, SVAssertions
- Supports: HSpice, HSIM, PSpice, SmartSpice, NanoSim, UltraSim, FineSim, Spectre, HSIM, Eldo, GridSim in all formats
- The EDA industry's only complete tool for analog, digital and mixed-mode designers, is also the easiest to use

Veritools

Download vWave from our web site: www.veritools.com

330 Lunada Drive, Los Altos, CA 94022; phone: (650) 533-5595; e-mail: schop@earthlink.net

Wave is the only universal and the only complete waveform display and analysis tool for analog, digital and mixed analog-digital designs

vWave (Undertow)-Universal

vWave-Universal runs native on Windows and on Linux. This makes vWave is the only universal waveform display tool, for virtually all analog and digital simulators. Very fast PLI/VPI routines are also available to support almost all digital simulators including Synopsys VCS, Cadence NCSIM, Mentor Modeltech, and many others and allow for signal file compression of thousands of times. API routines are also available to provide direct output of highly compressed utF files already support almost most widely used analog simulators including HSIM, FineSim UltraSim, GridSim and others. Directly reading many the simulators native output is also available for file from Synopsys HSpice, Mentor Eldo, Cadence Specter and SmartSpice.

This software is targeted at users with mixed analog/digital designs and users who want to bring in any combination and an unlimited number of signal files from these simulators at the same time. Included in this software are hundreds of functions for digital designers and several hundreds of functions for analog designers including:

A complete set of Analog Arithmetic Functions including:

- Trigonometry Functions, sin, tan, etc.
- FFT, DFT
- Integrate/Differentiate Functions
- Filter, Elliptic, Bessel, Butterworth,

Chebyshev, FIR, IIR, etc.

• Frequency, Jitter, Period verses Time

vWave supports Perl, TCL, and Python scripting.

Veritools also provides:

VeritoolsDesigner-Universal

VeritoolsDesigner adds to vWave a complete RTL source code debugging environment for Verilog, SystemVerilog, VHDL, with the following windows;

Unlimited waveform windows, Source Code debugging windows, schematic windows, control flow graph windows and state diagram windows with automatic state diagram extraction from the source code.

VeritoolsVerifyer-Universal

VeritoolsVerifyer adds to VeritoolsDesigner a complete set of tools to allow verification engineers to debug their SystemVerilog Assertions. Included is a standalone **SystemVerilogAssertion** Simulator, and SVA analyzer, so user can see the color-coded results for their assertions, a "what if window" so users can not only see the exact part of the assertion that is causing any failing assertion to fail evaluation, but can use edit window to modify the part of any assertion that was assertion evaluation failing and then instantly retest the modified assertion. Users can do this an unlimited number of software times. This also includes automatic coverage and result analysis tools so users can quickly see what coverage they are getting with their assertions. Coverage results are generated for each and every simulation with no slow-down in simulation speed.

vWave is the world's only universal waveform display tool for analog, digital and mixed-mode designs.

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