

Announcement for DAC 2019

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VERITOOLS ANNOUNCES FOR DAC 2019

Veritools internal libraries available now for use in user's tools suites:

These libraries now include:

Waveform viewing for all analog and digital simulators

Source Code Viewing and debugging and/or with;

State Diagram Viewing, from Verilog, SystemVerilog source code

RTL and Gate Schematic viewing, from RTL/Gate source code, with user's own symbols and components

Standalone SystemVerilog Assertion simulation and analysis

Libraries available for Linux, Centos 5,6,7 and Ubuntu and native Windows 7,8, and 10.

Veritools is a leading provider of waveform display, source code debugging and SVA assertions tools for the digital and mixed integrated circuits markets. Veritools software has been used in over 350 companies in 45 countries. For more information please visit our DAC booth, email inquiry@veritools.com, or schop@earthlink.net, or call 650 533-5595 or visit <http://www.veritools.com>.

